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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/591,615	06/09/2000	Laurent Six	TI-29030	2796
7590	10/17/2003		EXAMINER	BATAILLE, PIERRE MICHE
Gerald E Laws Texas Instruments Incorporated P O Box 655474 MS 3999 Dallas, TX 75265			ART UNIT	PAPER NUMBER
			2186	
DATE MAILED: 10/17/2003				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/591,615	SIX ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Pierre-Michel Bataille	2186

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 29 September 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

- a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2.  The proposed amendment(s) will not be entered because:
  - (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  they raise the issue of new matter (see Note below);
  - (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_.

3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: see attached.
6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: none.

Claim(s) objected to: none.

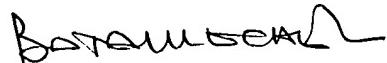
Claim(s) rejected: 1-12.

Claim(s) withdrawn from consideration: none.

8.  The proposed drawing correction filed on \_\_\_\_\_ is a) approved or b) disapproved by the Examiner.

9.  Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s). \_\_\_\_\_.

10.  Other: Certified Copy of Priority Application received



Pierre-Michel Bataille  
Primary Examiner  
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## ADVISORY ACTION

### *Response to Arguments*

1. Applicant's arguments and request for reconsideration filed September 29, 2003 have been fully considered but they do not place the application in condition for allowance for at least the reasons below.

The claims (claim 1 and 8) call for selecting a first portion of memory in accordance with a size parameter and to limit access to the selected portion. Applicant argues that Pawate et al (US 5,638,530) does not have such teaching. In contrast to applicant's remark, Pawate teaches that the first 2K bytes of the host computer are not available to the host computer for shared memory access when in the smart mode. The limiting access to a first portion of the claims is met by Pawate, as the host computer cannot access the first kilobytes of the shared memory while the card is in the first mode, whereas, the host computer cannot access, by reading and writing, the first 2K bytes of the shared memory in the smart mode.

Pawate teaches: access to the registers provides selective access of the first 2K bytes, accessed exclusively or protectively by the DSP in smart mode, while the host computer access is permitted access to the remaining memory portion; the use of control registers for shared or standard mode and the DSP (170) to communicate with the host computer (200) by the communication registers, the registers being accessed for control and mapping (Col. 7, Lines 8-30); the host computer always having the higher priority for accessing the shared memory (Col. 13, Lines 29-33); standard mode

or shared access implies that access is permitted by the host processor and the DSP where arbitration is required and where the host computer is always assigned higher priority.

Pawate teaches placing the second portion of the memory circuit in a low power mode minimizing power consumption of the smart card while in the smart mode until an external event, allowing the host computer to have quicker access to the remaining unused portion on the card) [Col. 9, Lines 53-56; Col. 14, Lines 3-13].

Boutaud (US 5,838,934) teaches a selection circuit connected to host port internal data and control bus and to synchronizer logic, wherein control logic is used to determine the type of access, shared access mode (SAM) or host only mode (HOM) (Col. 12, Lines 32-36) where control logic 130a generates memory control synchronous memory control signals when shared access mode is required, such that both the first requester circuit and the second requester circuit can sequentially access the memory circuit when the priority circuitry indicates a first relative priority state between the first priority and the second priority (Col. 13, Lines 25-40); when in shared access mode (SAM) access by both the host 400 and the processor 300 are synchronized to clock signals to avoid conflicts (Col. 8, Lines 28-31, 59-67; Col. 9, Lines 1-18; Col. 10, Lines 48-51; Col. 16, Lines 14-27).

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre-Michel Bataille whose telephone number is (703) 305-0134. The examiner can normally be reached on Tue-Fri (7:30A to 6:00P).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew M. Kim can be reached on (703) 305-3821. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Pierre-Michel Bataille  
Primary Examiner  
Art Unit 2186